

Notice of Allowability

Application No.

09/976,206

Examiner

Duc T. Duong

Applicant(s)

PARRUCK ET AL.

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 4/13/07.
2. ☒ The allowed claim(s) is/are 45-49 and 52-55; now renumbered 1-9, respectively.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date <u>7-31-07</u> |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in an interview with Alyssa Finamore on July 31, 2007.

2. The application has been amended as follows:

In claim 45, line 4, after the word "and" inserted --initially--.

In claim 45, line 11, after the comma inserted --each having a single data extending from a first bus interface, through segmentation circuitry, through reassembly circuitry, and to a second bus interface--.

In claim 45, line 12, after the word "memory" inserted --available for storing network information flowing on the data path of the first integrated circuit--.

In claim 45, line 19, after the word "cell" inserted --on the data path of the second integrated circuit--.

In claim 45, line 21, after the word "on" inserted --the data path of the third integrated circuit of--.

In claim 45, line 26, after the word "information" inserted --on the data path of the fourth integrated circuit--.

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In claim 45, lines 28-30, removed the phrase "wherein each of the first, second, third, and fourth integrated circuits is an integrated circuit that has a single data path, the single data path extending from a first bus interface, through segmentation circuitry, through reassembly circuitry, and to a second bus interface" and inserted "--wherein network information communicated on the data paths of the first, second, third, and fourth integrated circuits is sent through the respective segmentation circuitry, reassembly circuitry of the integrated circuits--".

The above examiner's amendment was made to place the claim in condition of allowance.

Allowable Subject Matter

3. Claims 45-49 and 52-55 are allowed.

The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or make obvious the steps of or means for **"wherein network information communicated on the data paths of the first, second, third, and fourth integrated circuits is sent through the respective segmentation circuitry, reassembly circuitry of the integrated circuits"**, when such data paths are considered within the specific structure of the method recited in claim 45. The prior art of record fails to teach or make obvious the steps of or means for **"wherein the ingress device of the first line card is a multi-service segmentation and reassembly device, the multi-service segmentation and reassembly device receiving both flows of packets and cells"**, when such ingress device is considered within the specific structure of the device recited in claim 52. The prior art of record fails to teach

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or make obvious the steps of or means for **“wherein each of the first, second, third, and fourth integrated circuits is an integrated circuit that has a single data path, the single data path extending from a first bus interface, through segmentation circuitry, through reassembly circuitry, and to a second bus interface”**, when such integrated circuits are considered within the specific structure of the device recited in claim 55.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Duc T. Duong whose telephone number is 571-272-3122. The examiner can normally be reached on M-F (9:00 AM-6:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wing Chan can be reached on 571-272-7493. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

DD
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8/16/07
WING CHAN
SUPERVISORY PATENT EXAMINER